



Zew AF
S

**NOTICE OF APPEAL FROM THE EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

TI-35951

In re application of:

Christo P. Bojkov et al.

Serial No.: 10/689,386

For: Direct Bumping on Integrated Circuit Contacts
Enabled by Metal-to-Insulator Adhesion

Filed: 10/20/2003

Group Art Unit: 2811

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision in the Office Action dated 04/28/2005 of the Primary Examiner finally rejecting claims 1-22.

The item (s) checked below are appropriate:

1. An extension of time to respond to the final rejection
 was granted on _____
 is requested for _____ month(s).
2. A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. Fee \$500.00:
 Not required (*Fee paid in prior appeal*)
 The Commissioner is hereby requested to grant any time extension that may be due, and is authorized to charge any fees which may be required incident to this filing, or credit any overpayment to Deposit Account No. 20-0668.

Signature [Rule 191(b)]

Post Office Address
(to which correspondence is to be sent)

07/05/2005 WABDELR1 00000032 200668 10689386

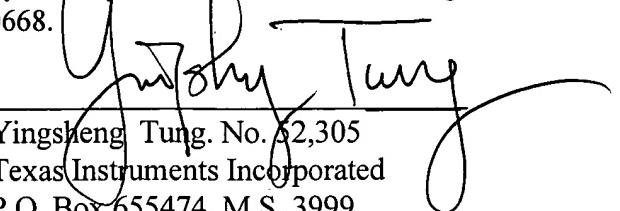
01 FC:1401 500.00 DA

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 or is being facsimile transmitted to the USPTO on the date indicated below:

6-28-2005

Jackie McBride
Jackie McBride


Yingsheng Tung, No. 62,305
Texas Instruments Incorporated
P.O. Box 655474, M.S. 3999
Dallas, TX 75265